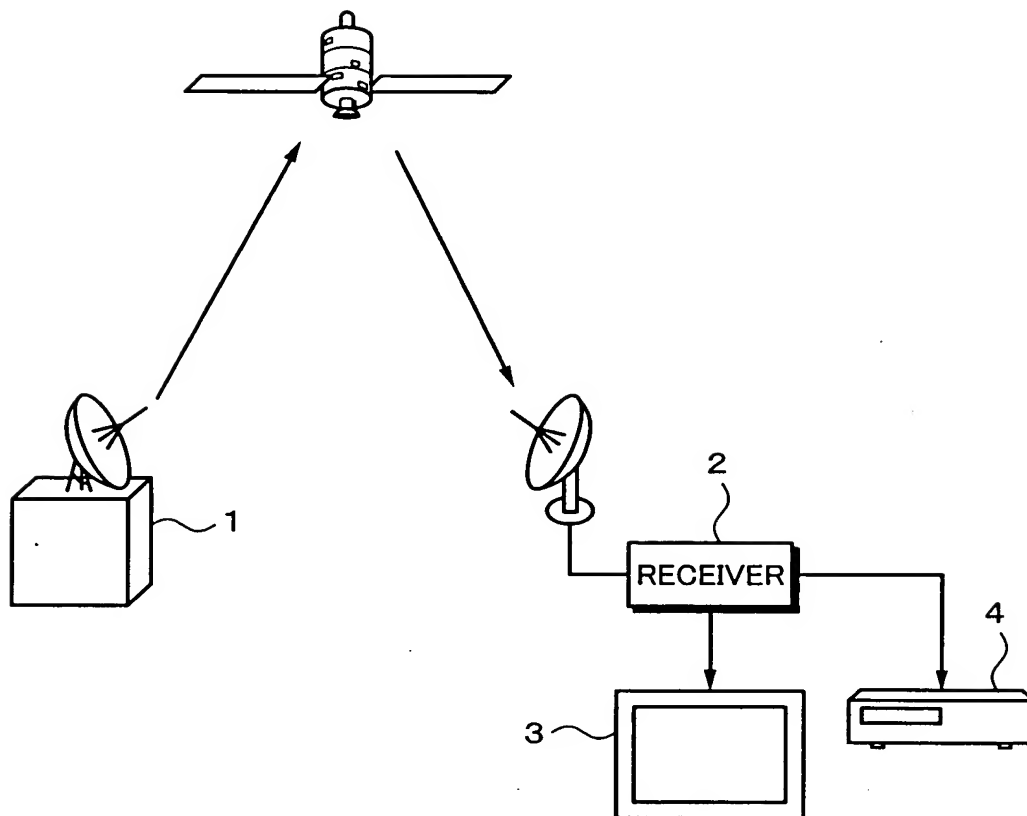


Fig. 1

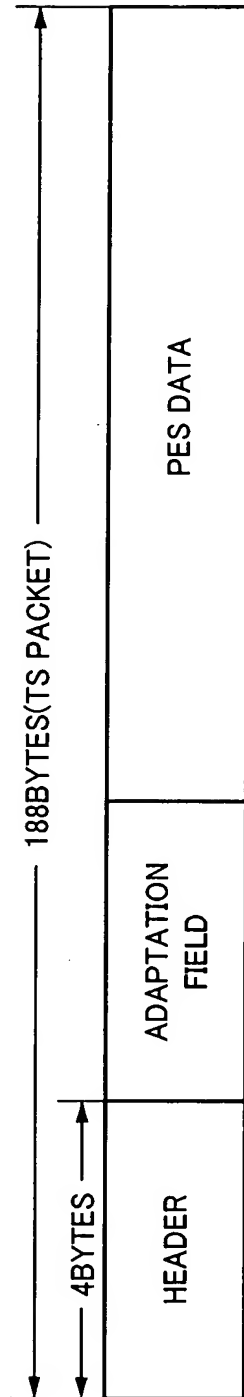
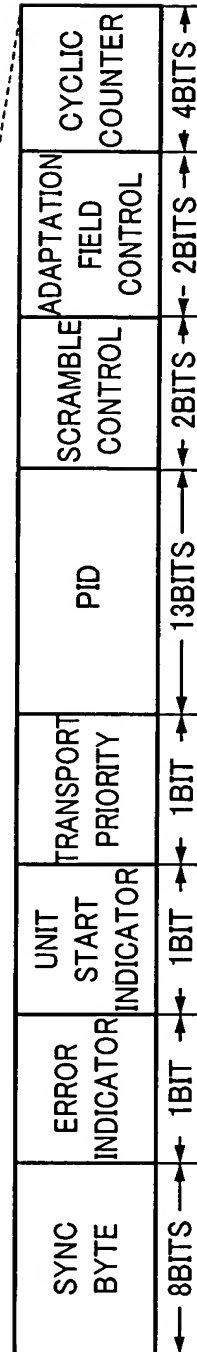
**Fig. 2A****Fig. 2B**

Fig. 3A

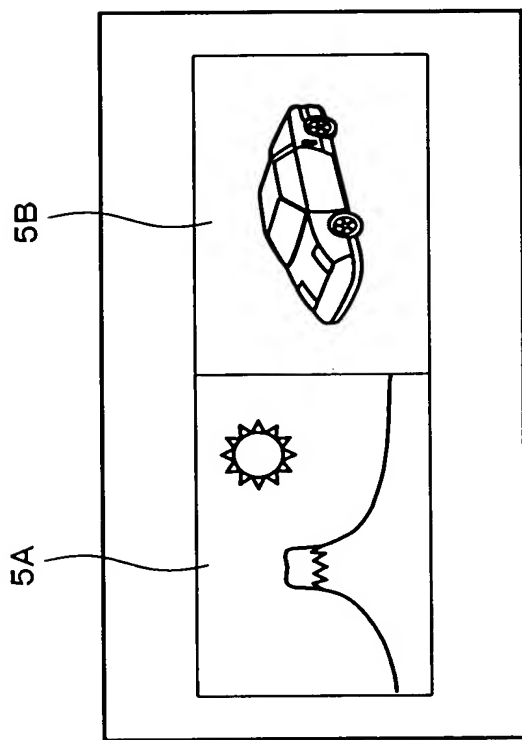


Fig. 3B

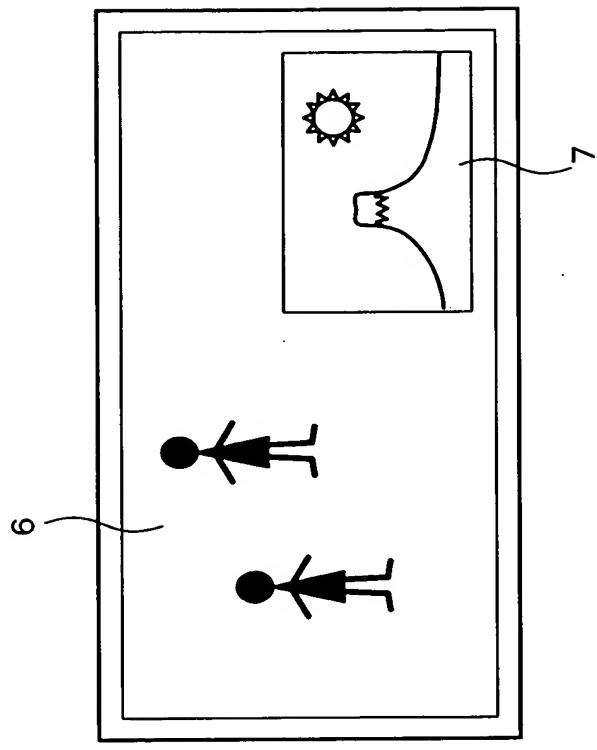


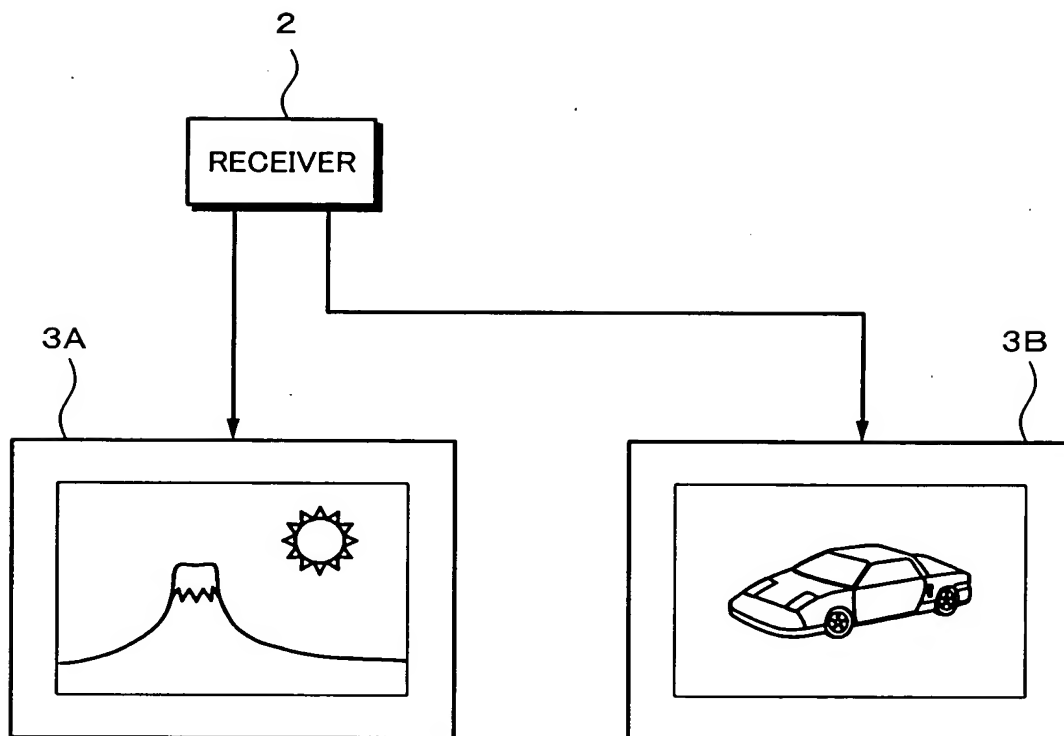
Fig. 4

Fig. 5

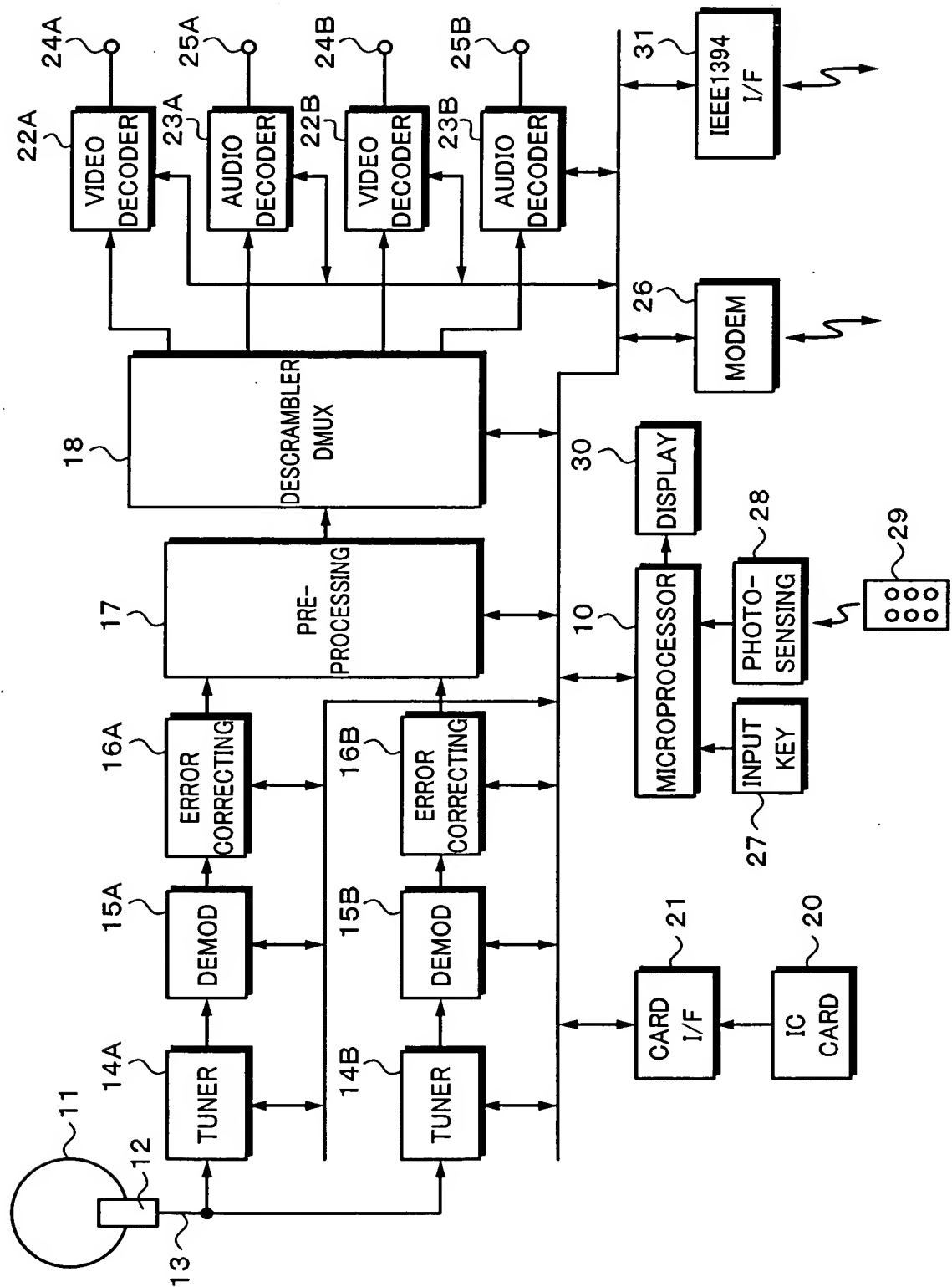


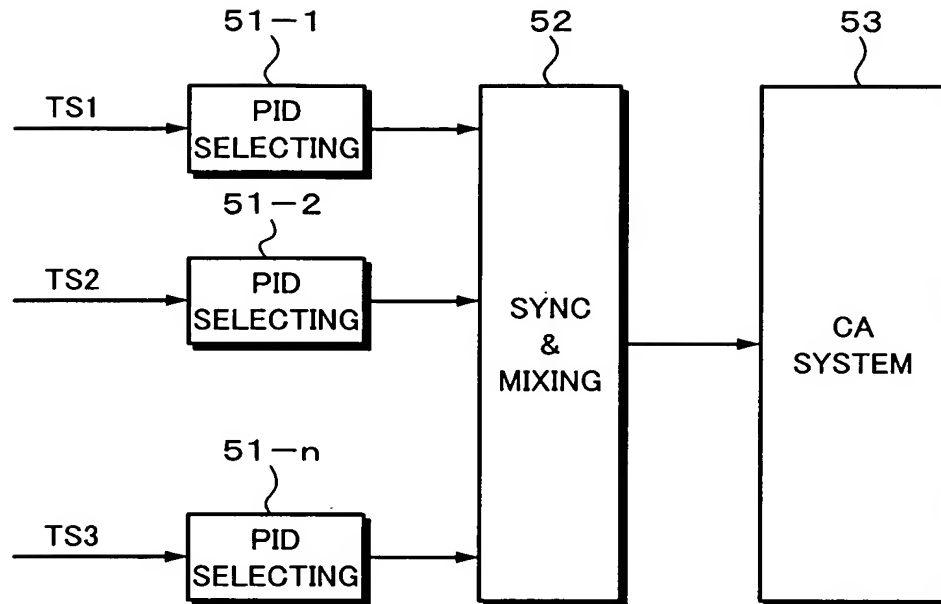
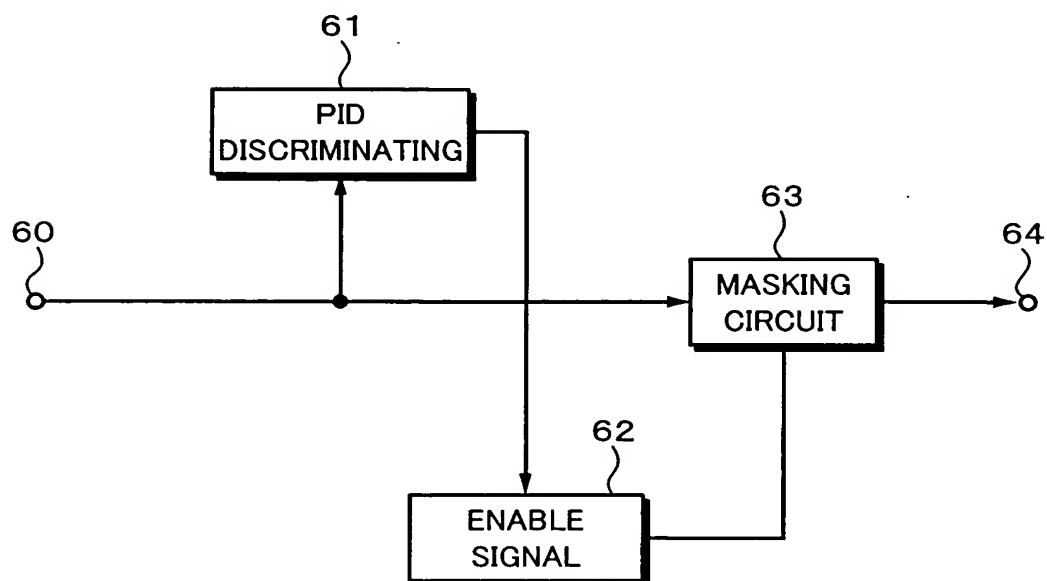
Fig. 6**Fig. 7**

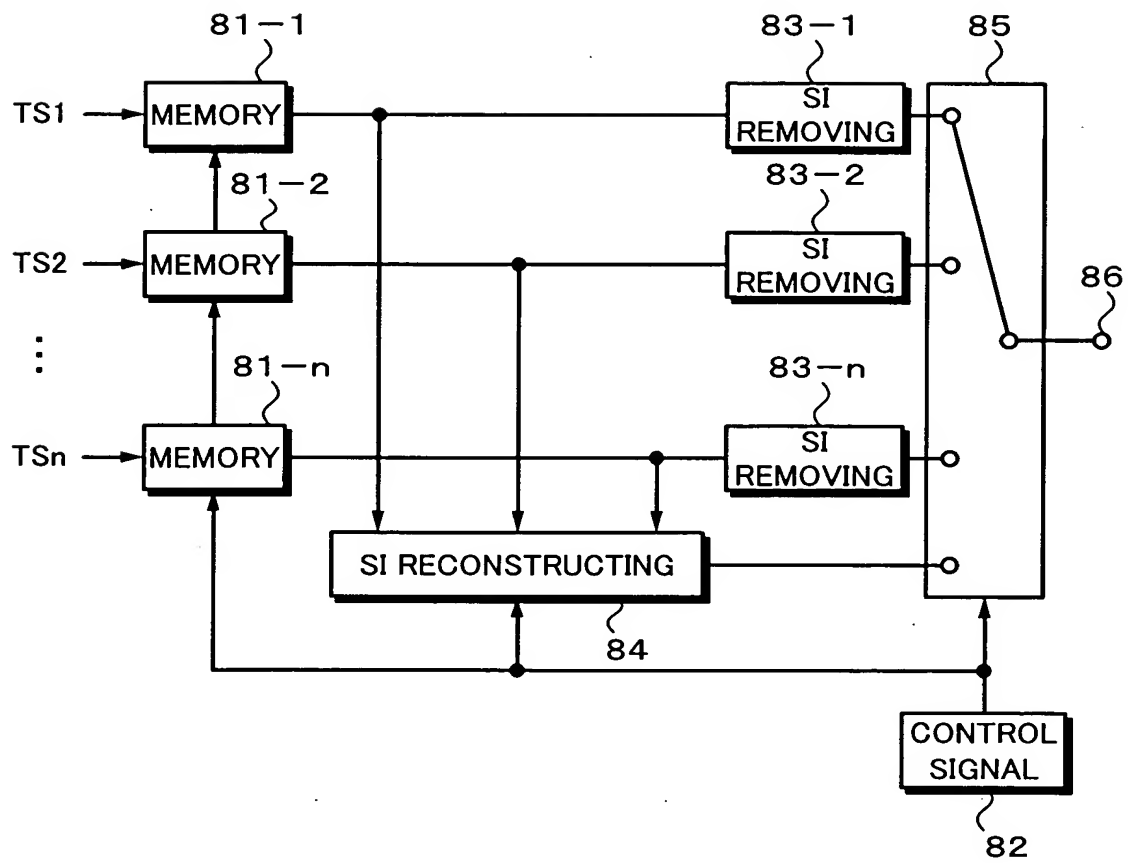
Fig. 8

Fig. 9

PAT	
91	<div>Version_number</div> <div>current_next_indicator</div> <div>section_number</div> <div>last_section_number</div> <div>HEADER INFORMATION OF PAT</div>
92	<div>PROGRAM1</div> <div>PROGRAM NUMBER(program_number)</div> <div>PID OF PMT</div> <div>INFORMATION OF EACH PROGRAM</div>
	<div>PROGRAM2</div> <div>PROGRAM NUMBER(program_number)</div> <div>PID OF PMT</div>

93	<div>CRC_32</div> <div>ERROR DETECTION</div>

Fig. 10A



Fig. 10B

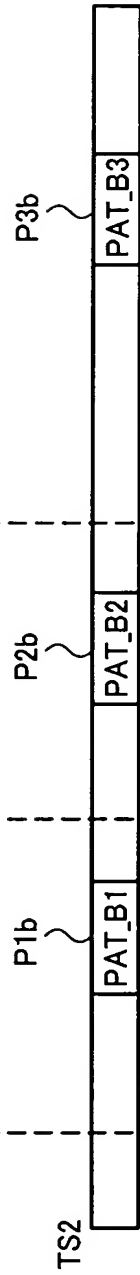


Fig. 10C

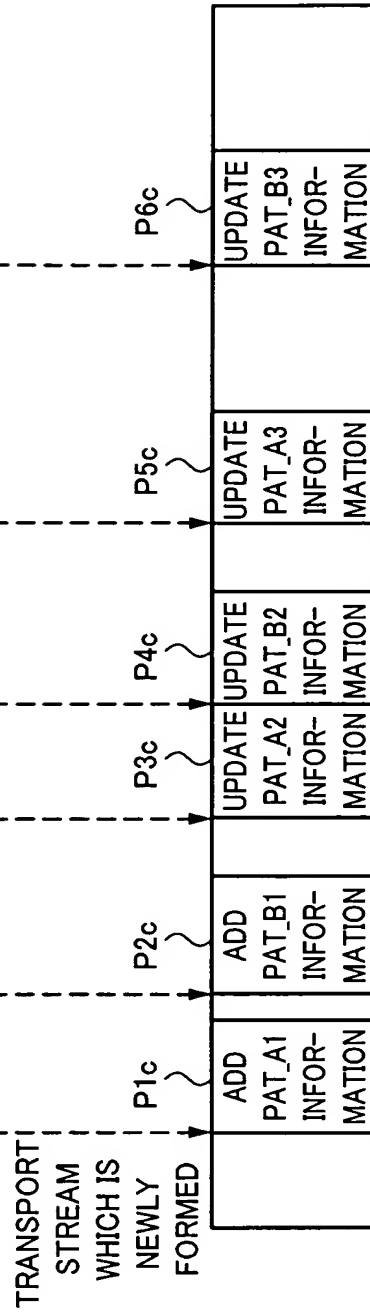


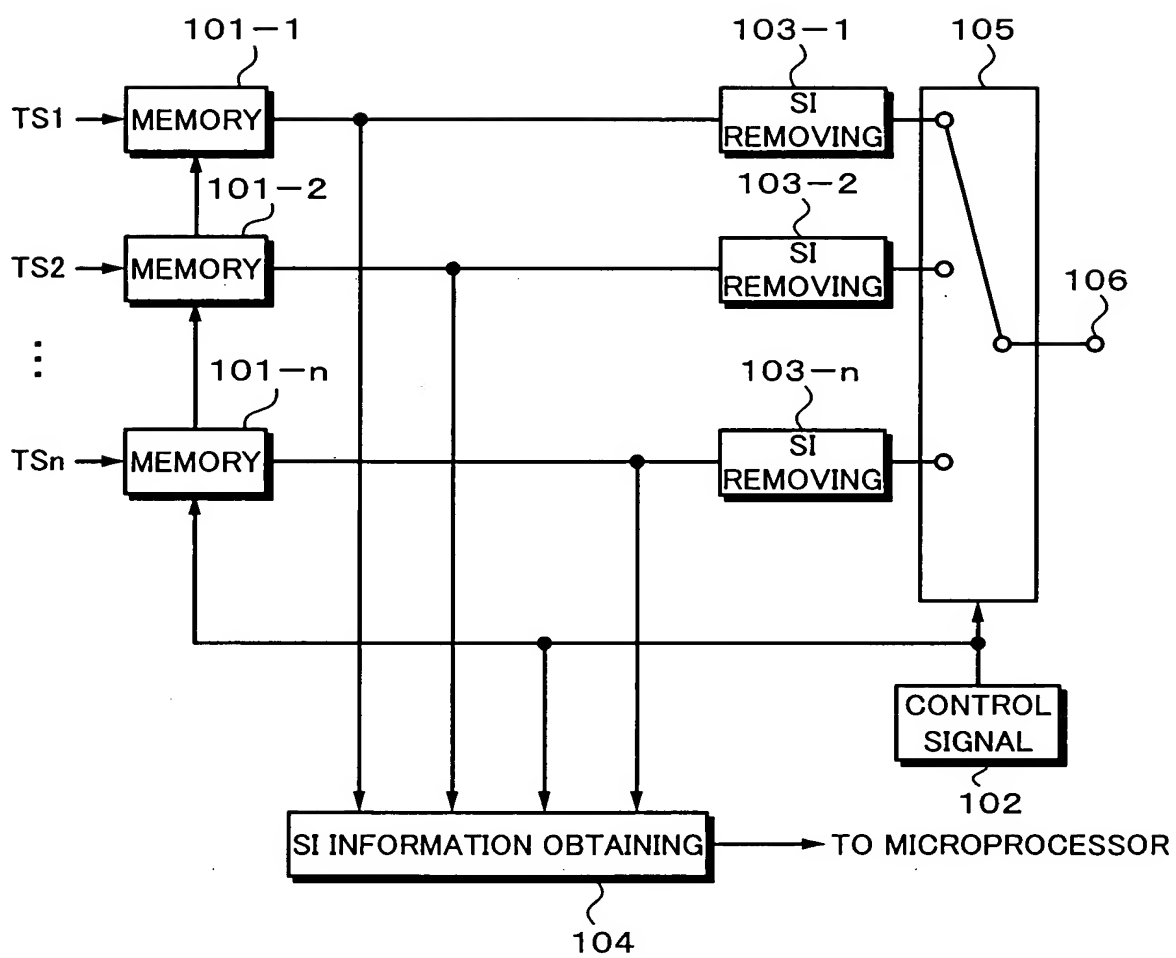
Fig. 11

Fig. 12

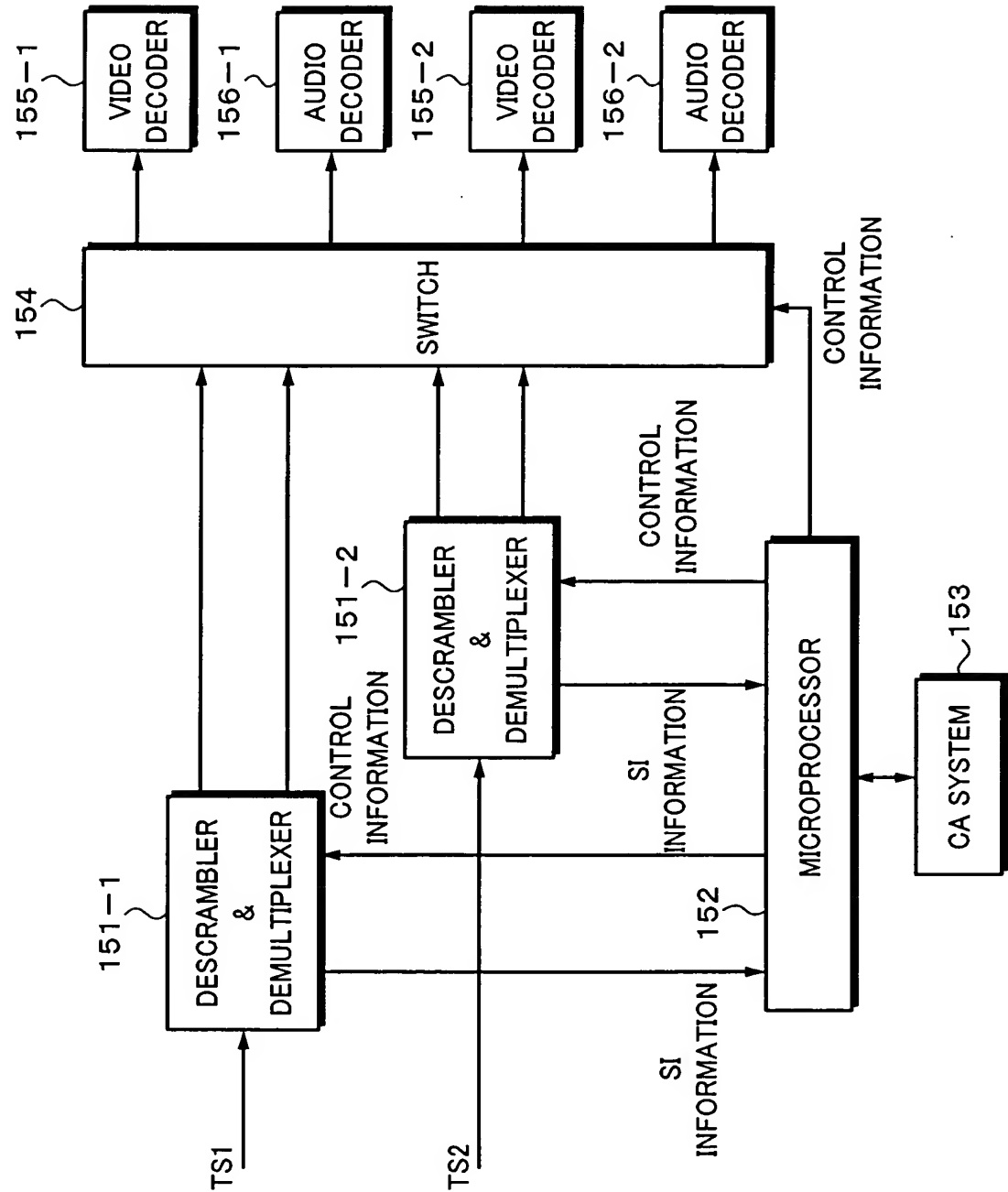
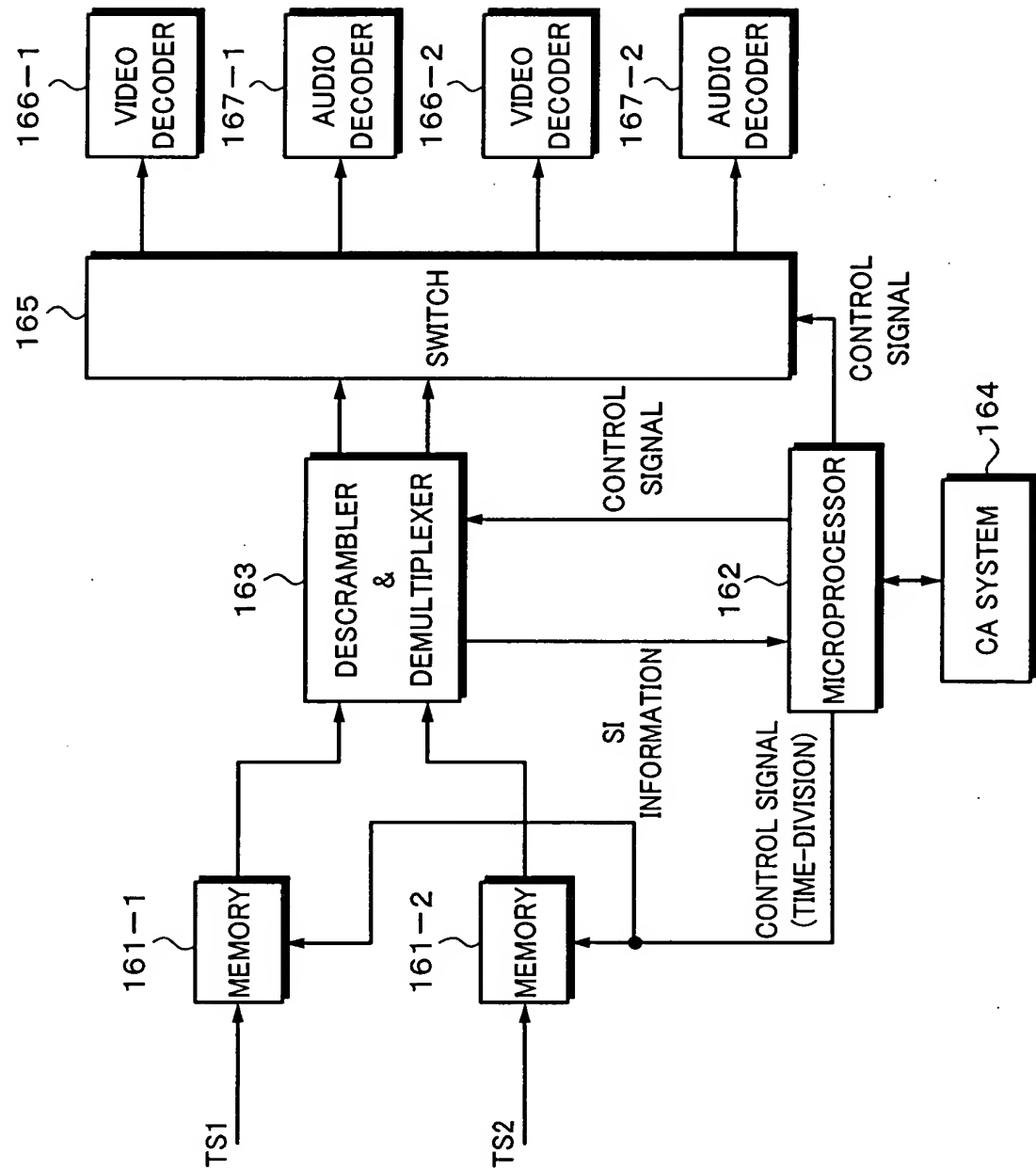


Fig. 13



DESCRIPTION OF REFERENCE NUMERALS

- 17 PREPROCESSING CIRCUIT
- 51-1, 51-2, ..., 51-n SELECTING CIRCUIT
- 52 SYNC & MIXING CIRCUIT
- 53 CA SYSTEM
- 61 PID DISCRIMINATING CIRCUIT
- 62 ENABLE SIGNAL SETTING CIRCUIT
- 63 MASKING CIRCUIT